



IRW

In re: Application of:
Chih-Mu Huang Group Art Unit: 2815
Serial No.: 10/723,072 Examiner: Brock II, Paul E.
Filed: Nov. 25, 2003 In Response to Office Action
Dated: Feb. 15, 2005
For: Single Transistor Ram Cell and Method of Manufacture
Attorney Docket No.: 67,200-1178

Certificate of Mailing

I hereby certify that this paper is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date: Mar. 8 2005

Kathy Dixon

RESPONSE TO OFFICE ACTION

Commissioner for Patents
Alexandria, VA 22313-1450

Dear Sir:

In response to an Office Action mailed Feb. 15, 2005 of a restriction requirement imposed by the Examiner, the Applicants hereby elect with traverse the prosecution of Group I, method claims 1-21.